

EIT 2009 TECHNICAL PROGRAM

Monday Morning Sessions: M1A, M2A, M3A, M4A

Monday Afternoon Sessions: M1P, M2P, M3P, M4P, M5P, M6P

Tuesday Morning Sessions: T1A, T2A, T3A, T4A

Tuesday Afternoon Sessions: T1P, T2P, T3P, T4P, T5P, T6P

M1A (10:30 – 12:00 noon): Communication Systems I

Chair: *TBD*

Room: *Malden*

10:30 *Modulation and Directionality Characteristics of Free-space Optical Transmission Links (Paper # 47)*

Xian Jin, Christopher Collier, Jonathan Holzman, Julian Cheng
(School of Engineering, University of British Columbia Okanagan)
Kelowna, Canada

10:48 *High-Speed CRC Computation Using Optimum State-Space Transformations (Paper # 103)*

Christopher Kennedy and Arash Reyhani-Masoleh
Department of Electrical and Computer Engineering
The University of Western Ontario, London, ON N6A 5B9 Canada

11:06 *Effect of SNR Mismatch on the Performance of Turbo Decoding Algorithms (Paper # 125)*

Behnam Shahrava and Yibo Zhou (University of Windsor, Windsor, Ontario)

11:24 *Joint Source-Channel Coding using Finite State Integer Arithmetic Codes (Paper # 67)*

Hashem Moradmand Ziyabar, Ali Payandeh, Mohammad Reza Aref
(Sharif University of Technology, Tehran, Iran)

M2A (10:30 – 12:00 noon): Mobile Networks

Chair: *Jian Ren*

Room: Belle River

10:30 *Market-based computational task assignment within autonomous wireless sensor network (Paper # 55)*

Andrew T. Zimmerman and Jerome P. Lynch (University of Michigan) Ann Arbor, Michigan, USA,

Frank T. Ferrese (Naval Surface Warfare Center, Carderock Division) Philadelphia, Pennsylvania, USA

10:48 *Routing-Based Source-Location Privacy Protection in Wireless Sensor Network (Paper # 46)*

Yun Li, Leron Lightfoot, Jian Ren (Department of Electrical and Computer Engineering, Michigan State University), East Lansing, Michigan, USA

11:06 *Architectural Model for Wireless Peer-to-Peer(WP2P) File Sharing for Ubiquitous Mobile Devices (Paper # 14)*

O.O. Abiona, A. I. Oluwaranti, T. Anjali, C. E. Onime, E.O. Popoola, G.A. Aderounmu, W.J. Dorin and L.O. Kehinde, (Computer Information Systems Department, Indiana University Northwest), Garry, Indiana, USA

11:24 *A New Call Admission Control Scheme Based on New Call Bounding and Thinning II schemes in Cellular Mobile Networks (Paper # 19)*

Zahra Firouzi, Hamid Beigy, (Department of Computer Engineering Sharif University of Technology), Tehran, Iran

M3A (10:30 – 12:00 noon): Computer Networks & Security I

Chair: *TBD*

Room: Tilbury

10:30 *StackLock with Simple FSM (Paper # 41)*

Dongkyun Ahn and Gyungho Lee, (Department of Electrical and Computer Engineering University of Illinois at Chicago), Chicago, Illinois, USA

10:48 *A Low-Cost S-box for the Advanced Encryption Standard Using Normal Basis (Paper # 144)*

Mehran Mozaffari-Kermani and Arash Reyhani-Masoleh
Department of Electrical and Computer Engineering
The University of Western Ontario, London, ON N6A 5B9 Canada

11:06 *The Implementation of Secure Canary Word for Buffer-Overflow Protection (Paper #98)*

Sirsara Chiamwongpaet and Kerk Piromsopa (Dept. of Computer Engineering, Chulalongkorn University), Bangkok, Thailand

11:24 *A Secure E-Tendering system (Paper #89)*

Shahriyar Mohammadi and Hedy Jahanshahi (Faculty of Industrial Engineering, K.N. Toosi University of Technology), Tehran, Iran

11:42 *Interesting patterns using goal programming (Paper # 68)*

Yasir Imtiaz Khan, Asim Raza, and Syed Hasnain Haider, (Faculty of Information Technology, University of Central Punjab), Lahore, PAKISTAN

M4A (10:30 – 12:00 noon): Information Technology and Management

Chair: *M. Fathi*

Room: *Essex*

10:30 *A Data Mining based Knowledge Management Approach for the Semiconductor Industry (Paper # 84)*

C. Sassenberg, C. Weber, M. Fathi, R. Montino (University of Siegen), Siegen, Germany

10:48 *Knowledge Management Support for Quality Management to Achieve Higher Customer Satisfaction (Paper # 86)*

Fazel Ansari, Marjan Khobreh, Sara Nasiri and Madjid Fathi (Institute of Knowledge Based Systems, University of Siegen), Siegen, Germany

11:06 *Elements of Methods of Problem Solving and the Resource Capability in Software Engineering Paper # 35)*

Peter H. Chang, (Lawrence Technological University), Michael Blaha, Southfield, Michigan, USA

11:24 *A New Probing Scheme for Fault Detection and Identification (Paper # 56)*

Otman Basir and Abduljalil Mohamed (Systems Design Engineering Department, University of Waterloo), Waterloo, Ontario, Canada

11:42 *An Algorithm to Determine Neighbor Nodes for Automatic Human Tracking System (Paper # 42)*

H. Kakiuchi (Melco Power Systems Co., Ltd.), T. Kawamura, T. Shimizu, and K. Sugahara, Kobe, Japan

M1P (1:30 – 3:00 pm): Advanced Control Systems and Applications I

Chair: *Xiang Chen*

Room: *Tilbury*

1:30 *A Anesthesia patient monitoring and control in wireless based systems, (Paper # 132)*

Le Yi Wang, Zhibin Tan, and Hong Wang, (Wayne State University), Detroit, Michigan, USA

1:48 *A Technique for Using H₂ and H-infinity Robust State Estimation on Nonlinear Systems (Paper # 129)*

G. K. Lowe and M.A. Zohdy, Oakland University, Rochester, Michigan, USA

2:06 *Nonlinear Extension Study for Analytic Multi-Objective Control Design (Paper # 130)*

Chen Gao, Hugh H. T. Liu, and Ping Zhang, University of Toronto, Toronto, Ontario, Canada

2:24 *Robust Control of Tone Reproduction Curves for the Xerographic System (Paper # 133)*

Xiaobo Li and Kemin Zhou, Louisiana State University, Baton Rouge, Louisiana, USA

2:42 *Fault Tolerant Control of Electric Power Steering using Kalman Filter-Simulation Study (Paper 131)*

Smitha Cholakkal and Xiang Chen, (ECE Dept., University of Windsor), Windsor, Canada

M2P (1:30 – 3:00 pm): Image Processing I

Chair: *TBD*

Room: Essex

1:30 Using stereo geometry towards accurate 3D Reconstruction (Paper # 90)

Z. Wang and B. Boufama, (School of Computer Science, University of Windsor), Windsor, Ontario, Canada

1:48 Beamlet Transform Based Technique for Pavement Image Processing and Classification (Paper # 45)

L. Ying and E. Salari (Dept. of Electrical Engineering and computer Science, University of Toledo), Toledo, Ohio, USA

1:06 A Low-Cost, Real-time, Hardware-Based Image Demosaicking Algorithm (Paper #104)

Anthony Karloff and Roberto Muscedere, (Department of Electrical and Computer Engineering, University of Windsor), Windsor, Ontario, Canada

2:24 Color Restoration Techniques for Faded Colors of Old Photos, Printings and Paintings (Paper # 128)

Ayman M. T. Ahmed (National Authority of Remote Sensing and Space Sciences), Cairo, Egypt

2:42 A Feature Based Approach for Localization of Indian Number Plates(paper # 92)

Prathamesh Kulkarni, Ashish Khatri, Prateek Banga, Kushal Shah (Dept. of Electronics and Telecommunication University of Pune), India

M3P (1:30 – 3:00 pm): Signal Processing

Chair: TBD

Room: Malden

1:30 Iterative Design of IIR Variable Fractional Delay Digital Filters (Paper #75)

Aimin Jiang and Hon Keung Kwan, (Dept. of Electrical Engineering, University of Windsor), Windsor, Ontario, Canada

1:48 Recognition Strategies for General Handwritten Text Documents (Paper #142)

M. Shridhar and G. F. Houle, F. Kimura (University of Michigan-Dearborn), Dearborn, Michigan, USA

2:06 Document Recognition Strategies for Bank Cheques (Paper #141)

M. Shridhar and G. F. Houle, F. Kimura (University of Michigan-Dearborn), Dearborn, Michigan, USA

2:24 Joint MCMA and DD blind equalization algorithm with variable-step size (Paper #57)

Doaa Ashmawy, Kevin Banovic, Esam Abdel-Raheem, Mohamed Youssif Hala Mansour and Mahmoud Mohanna, (ECE dept., Benha University), Cairo, Egypt

M4P (3:30 – 5:00 pm): MEMS and Nanotechnology

Chair: *TBD*

Room: *Tilbury*

3:30 A Highly Accurate Method to Calculate Capacitance of MEMS Sensors with Circular Membranes (Paper # 38)

Mosaddequr Rahman, Sazzadur Chowdhury
(Dept. of Electrical and Computer Engineering, University of Windsor),
Windsor, Ontario, Canada

3:48 A Single-Pole-Triple-Throw (SP3T) MEMS RF Switch for 24 GHz Short Range Radar (Paper # 16)

Ahmad Sinjari, Sazzadur Chowdhury (Dept. of Electrical and computer Engineering, University of Windsor), Windsor, Ontario, Canada

4:06 Sensitivity of tungsten oxide thin films for nitric oxide and methane gases (Paper # 64)

Ahalapitiya H. Jayatissa and Lash Mapa, (Mechanical, Industrial, and Manufacturing Engineering (MIME) department at The University of Toledo), Toledo, Ohio, USA

4:24 Finite Element Simulation of SMFBAR based Sensor (Paper # 136)

Chi-Jung Cheng and Massood Zandi Atashbar (Department of Electrical and Computer Engineering, Western Michigan University), Kalamazoo, Michigan, USA

4:42 Employing Work Function Engineering and Asymmetric Gate Oxide in Nano-Scale Source- Heterojunction-MOS-Transistor (Paper # 85)

Mahsa Tahermaram, Mahdi Vadizadeh (Department of Electrical and Computer Engineering, University of Tehran), Tehran, Iran

5:00 Design and Implementation of MEMS Based Differential Voltage Controlled Oscillator (paper # 93)

B. S. Sreeja and S. Radha,SSN college of engineering

M5P (3:30 – 5:20 pm): Circuits, Systems and Neural Networks

Chair: *TBD*

Room: *Malden*

3:30 *On Linear Time-Varying System Characterization (Paper # 87)*

S. Erfani, (Electrical and Computer Engineering Dept., University of Windsor), Windsor, Ontario, Canada

3:48 *Building a Neuro-Fuzzy Model for Predicting the Bacteria Bound and Internalized by the White Blood Cells (Paper # 48)*

Devinder Kaur, Jaina Sangtani (University of Toledo), Toledo, Ohio, USA

4:06 *Numerical calibration for 3-Axis accelerometers and magnetometers (Paper # 18)*

Frédéric Camps and Sébastien Harasse (University of Toulouse), France

4:24 *Thermal Characterization of the VBIC Dielectrically Isolated Device (Paper # 117)*

Md M. Hossain, W. Alan Davis, Howard T. Russell Jr., and Ronald L. Carter, Department of Electrical and Computer Engineering, St. Cloud State University, Minnesota.

4:42 *Mixed-Signal CVNS Adder for Two-operand Binary Addition (Paper #82)*

Mitra Mirhassani (Dept. of Electrical Engineering University of Windsor), Windsor, Ontario, Canada

M6P (3:30 – 5:20 pm): Digital Hardware Design & CAD I

Chair: *TBD*

Room: *Essex*

3:30 *Real-Time Invariant Textural Object Recognition with FPGAs (Paper #102)*

Timothy R. Pearson, (Electrical and Computer Engineering Department, Northern Illinois University), Illinois, USA

3:48 Opportunities for parallelism when implementing algorithms in VHDL - A Case Study - Shift-or (Paper #94)

Barry Schulz, Chirag Parikh and Christian Trefftz, (School of Engineering, Grand Valley State University), Grand Rapids, Michigan, USA

4:06 System-Level Memory Modeling for Bus-Based Memory Architecture Exploration (Paper #121)

Zhongbo Cao, Ramon Mercado and Diane T. Rover (Department of Computer Engineering, Iowa State University), Ames, Iowa, USA

4:24 Realization of Area Efficient QR Factorization Using Unified Division, Square Root, and Inverse Square Root Hardware (paper #119)

Semih Aslan, Erdal Oruklu, and Jafar Sanii (Electrical and Computer Engineering Department, Illinois Institute of Technology), Chicago, Illinois, USA

4:42 High Performance Signed-Digit Decimal Adders (paper #61)

Jeff Rebacz, Erdal Oruklu, and Jafar Sanii (Electrical and Computer Engineering Department, Illinois Institute of Technology), Chicago, Illinois, USA

T1A (10:30 – 12:00 noon): Wireless Systems

Chair: *TBD*

Room: *Tilbury*

10:30 Differential Access Points for Indoor Location Estimation (Paper # 28)

Ning Chang, Rashid Rashidzadeh, Majid Ahmadi (Dept. of Electrical Engineering University of Windsor), Windsor, Ontario, Canada

10:48 Jamming-Resilient Subcarrier Assignment for OFDMA Based Space-Time Coded Systems (Paper # 43)

Leonard E. Lightfoot, Lei Zhang, Jian Ren, and Tongtong Li (Department of Electrical & Computer Engineering Michigan State University), East Lansing, Michigan, USA

11:06 IP Mobility Scheme for Multi-hop WiMAX (Paper # 97)

Nicholas C. Doyle and Kemal E. Tepe, (Department of Electrical and Computer Engineering, University of Windsor), Windsor, Ontario, Canada

11:24 *Performance Analysis of Client/Server Versus Agent Based Communication in Wireless Sensor Networks For Health Applications (Paper # 107)*

David Barnes, Subra Ganesan and Suresh Sankaranarayanan, (Electrical and Computer Engineering Department, Oakland University), Rochester, Michigan, U.S.A.

11:42 *On the Power Efficiency and Optimal Transmission Range of Wireless Sensor Nodes (Paper # 139)*

Jin Zhu, (Electrical and Information Engineering Technology Department of Industrial Technology University of Northern Iowa), Cedar Falls, Iowa, USA

T2A (10:30 – 12:00 noon): Advanced Control Systems and Applications II

Chair: *Majid Fathi*

Room: Essex

10:30 *Noise Tolerance Improvement of The Linear Periodic Model Reference Adaptive Control Using Vigorous Probing (Paper # 63)*

Naghmeh Mansouri and Madjid Fathi (Department of Electrical and Computer Science, University of Siegen) Siegen, Germany

10:48 *Study and Implementation of a force stepper and Part Fit-up Solver for a Servo Controlled MFDC Weld Controller (Paper # 123)*

Dhanasekaran Venugopal, Manohar Das, and Vernon Fernandez (Department of Electrical and Computer Engineering, Oakland University), Rochester, MI, USA

11:06 *Robust Control Algorithm on a 16-bit dsPIC Processor (Paper # 134)*

Pavan K Vempaty and Nilanjan Roy Choudhury, (Department of Electrical and Computer Engineering, Oakland University), Rochester, Michigan, USA

11:24 *A design methodology for the implementation of embedded vehicle navigation systems (Paper # 66)*

Azizul Islam, J.M.Pierre Langlois and Aboelmagd Noureldin, Département de génie informatique et génie logiciel, École Polytechnique de Montréal, Montréal, Québec, Canada

T3A (10:30 – 12:00 noon): Biomedical Systems and Applications

Chair: *Kwan*

Room: *Malden*

10:30 *Detection of Body Posture with Low-Cost CMOS Camera Systems for Applications in Medical Care (Paper # 110)*

Yanfei Liu and Carlos Pomalaza-Ráez (Department of Engineering, Indiana University – Purdue University), Fort Wayne, Indiana, USA

10:48 *Numerical Representation of DNA Sequences (Paper # 112)*

Hon Keung Kwan and Swarna Bai Arniker, (Electrical and Computer Engineering Department, University of Windsor), Windsor, Ontario, Canada

11:06 *Graphical representation of DNA sequences (paper # 111)*

Swarna Bai Arniker and Hon Keung Kwan, (Electrical and Computer Engineering Department, University of Windsor), Windsor, Ontario, Canada

11:24 *Electrochemical Sensors for Detection of Biomolecules (Paper # 114)*

A. H. Jayatissa, Z. Li, L. Mapa, and A. C. Jayasuriya , (Electrical Engineering Dept., University of Toledo), Toledo, Ohio, USA

T4A (10:30 – 12:00 noon): Computer & Network Security II

Chair: *TBD*

Room: *Belle River*

10:30 *Watermarking of speech signals in the time-frequency domain (Paper # 30)*

Mahmood Al-khassaweneh, Hussein Al-Zoubi and Selin Aviyente (Department of Electrical and Computer Engineering Michigan State University), East Lansing, Michigan, USA

10:48 *Secure End-to-End Communication over GSM and PSTN Networks (Paper # 80)*

Saad Islam, Fatima Ajmal, Salman Ali, Jawad Zahid and Adnan Rashdi, (National University of Sciences and Technology), Pakistan

11:06 *A Test-bed for verification of vehicle safety communication applications (Paper # 122)*

Mohammad Naserian and Kurt Krueger, Vector CANtech, Novi, Michigan, USA

11:24 A New Fractional Call Admission Control Scheme in Integrated Cellular Network (Paper # 20)

Leila Mortazavi-Far and Hamid Beigy, (Department of Computer Engineering, Sharif University of Technology), Tehran, Iran

T1P (1:30 – 3:00 pm): Wireless Networks

Chair: *TBD*

Room: *Malden*

1:30 A Novel Real-time MAC Layer Protocol for Wireless Sensor Network Applications (Paper # 106)

Brajendra Kumar Singh and Kemal Ertugrul Tepe
(Dept. of Electrical and Computer Engineering, University of Windsor),
Windsor, Ontario, Canada

1:48 Characterization of the Adverse Effect of Neighborhood Capture in MANET and on the Way to a Remedy (Paper # 135)

Kazi Atiqur Rahman, Kazi Aminur Rahman, Matthias Lott, and Kemal Ertugrul Tepe (Dept. of Electrical Engineering University of Windsor),
Windsor, Ontario, Canada

2:06 A Joint Cross Layer Routing and Resource Allocation Algorithm for Multi-Radio Wireless Mesh Networks (paper # 143)

Taimour Aldalgamouni and Ahmed Elhakeem Department of Electrical and Computer Engineering, Concordia University Montreal, Canada

2:24 Effect of Factors on RFID Tag Readability-Statistical Analysis (Paper # 62)

Annaji Ammu, Lash Mapa, Ahalapitiya H. Jayatissa, (Industrial Engineering Technology Department at Purdue University), Calument, Indiana, USA

T2P (1:30 – 3:00 pm): Image Processing II

Chair: *TBD*

Room: *Tilbury*

1:30 A Cost Effective Probabilistic Approach To Localization And Mapping (Paper # 39)

Dibyendu Mukherjee, Ashirbani Saha, Pankajkumar Mendapara, Dan Wu and Q.M. Jonathan Wu, (ECE Dept., University of Windsor), Windsor, Ontario, Canada

1:48 *Fingerprint Image Compression Standard Based on Wave Atoms Decomposition and Self Organizing Feature Map (Paper # 36)*

Abdul A. Mohammed, Rashid Minhas, Q.M.Jonathan Wu, Maher A. Sid-Ahmed, (Dept. of Electrical and Computer Engineering, University of Windsor), Windsor, Ontario, Canada

2:06 *Automated Pavement Distress Detection Using advanced image Processing Techniques (Paper # 44)*

Y. Sun, E. Salari and E. Chou, (School of Computer Science, University of Toledo), Toledo, Ohio, USA

2:24 *A New Image Transform for a More Crop-Resilient NPT (Paper #51)*

Ayman M. T. Ahmed and Dwight D. Day (Department of Electrical and Computer Engineering, Kansas State University), Manhattan, Kansas, USA

T3P (1:30 – 3:00 pm): Digital Hardware Design & CAD II

Chair: *TBD*

Room: *Essex*

1:30 *Agile Hardware Development with Rapid Hardware Definition Language (Paper # 105)*

Jacob N. Allen, Hoda S. Abdel-Aty-Zohdy and Robert L. Ewing (Dept. of Elect. and Comp. Engineering, Oakland University), Rochester, Michigan, USA

1:48 *Artificial Neural Networks Activation Function HDL Coder (Paper # 40)*

Majid Ahmadi, Ashkan Hosseinzadeh Namin, Karl Leboeuf and Huapeng Wu (Dept. of Electrical Engineering University of Windsor), Windsor, Ontario, Canada

2:06 *FPGA-Based Design Of a High-Performance and Modular Video Processing Platform (Paper #58)*

Christophe Desmouliers, Erdal Oruklu and Jafar Saniie, (Department of Electrical and Computer Engineering, Illinois Institute of Technology), Chicago, Illinois, USA

2:24 *Selective Clock Gating by Using Wasting Toggle Rate (Paper # 116)*

Li Li, Ken Choi (Department of Electrical and Computer Engineering, Illinois Institute of Technology), Chicago, Illinois, USA

2:42 *Ultra-Low power and High Speed Design and Implementation of AES and SHA1 Hardware cores in 65 Nanometer CMOS Technology (Paper # 113)*

Ken Choi (Department of Electrical and Computer Engineering, Illinois Institute of Technology), Chicago, Illinois, USA

T4P (3:30 – 5:00 pm): Power Systems

Chair: *TBD*

Room: *Essex*

3:30 *Real-Time Observer for the Vector Control of an Induction Motor Drive (Paper # 109)*

Donald Gray, Zoltan Szekely, and Constantin Apostoiaia, (Dept. of Electrical and Computer Engineering, Purdue University Calumet), Hammond, Indiana, USA

3:48 *Neural Network Based Torque Control of Switched Reluctance Motor for Hybrid Electric Vehicle Propulsion at Low Speeds (Paper # 71)*

Dongyun Lu and Narayan C. Kar, (Dept. of Electrical Engineering University of Windsor), Windsor, Ontario, Canada

4:06 *A Neural Network based Approach for the Detection of Faults in the Brushless Excitation of a Synchronous Motor (Paper # 120)*

Donald Gray, Ziang Zhang, Constantin Apostoiaia, and Chang Xu, (Dept. of Electrical and Computer Engineering, Purdue University Calumet), Hammond, Indiana, USA

4:24 *Turbine Control System Upgrade For Bruce Nuclear Plant Units 1 and 2 (Paper # 138)*

Steven Frank Gray and Samir Basu, (Siemens, Power generation), Atlanta, Georgia, USA

T5P (3:30 – 5:00 pm): Digital Hardware Design & CAD III

Chair: *TBD*

Room: *Malden*

3:30 Fast Memory Addressing Scheme for Radix-4 FFT Implementation (Paper # 59)

Xin Xiao, Erdal Oruklu and Jafar Saniie (Department of Electrical and Computer Engineering, Illinois Institute of Technology), Chicago, Illinois, USA

3:48 NoC Prototyping on FPGAs: A Case Study Using an Image Processing Benchmark (Paper # 140)

Thuan Le and Mohammed A. S. Khalid , (Electrical and Computer Engineering Dept.), University of Windsor, Windsor, Ontario, Canada

4:06 Micro-Architectural Power Estimation and Optimization (Paper # 34)

Babak Hidaji, Mohamad Reza Andalibizadeh, Salar Alipour, (Computer Science and Engineering department, Chalmers University of Technology), Gothenburg, Sweden

T6P (3:30 – 5:20 noon): Reconfigurable and Embedded Systems

Chair: TBD

Room: Tilbury

3:30 UWindsor Nios II: A Soft-Core Processor for Design Space Exploration (Paper # 127)

Omar A. Al Rayahi and Mohammed A. S. Khalid (ECE Dept., University of Windsor), Windsor, Ontario, Canada,

3:48 Design of a Reconfigurable Robot-Based System for Material Deposition Applications (Paper # 74)

Ana Djuric and Jill Urbanic (IMSE Department, University of Windsor), Windsor, Ontario, Canada

4:06 Introducing Mechatronics Theme in the Undergraduate Mechanical Engineering Curriculum (Paper # 79)

Nader Zamani, (MAME Dept., University of Windsor), Windsor, Ontario, Canada

4:24 Performance Evaluation of SRAM Cells in 22nm Predictive CMOS Technology (Paper # 60)

David Hentrich, Erdal Oruklu and Jafar Saniie (Department of Electrical and Computer Engineering, Illinois Institute of Technology), Chicago, Illinois, USA

4:42 A Two-Type Data Cache Model A Two-Type Data Cache Model (Paper # 37)

Subha, S., Independent Consultant ?????